

ABSTRACT OF THE DISCLOSURE

A level detection circuit is provided with  
an arithmetic unit which multiplies an input signal  
Vin by a value, an integration circuit which integrates  
5 the result of computation by the arithmetic unit, and  
a comparison unit which compares the result of  
integration by the integration circuit and the input  
signal Vin and detects a signal level change of the  
input value Vin.